

FIG. 2
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241

SOURCE (R1)	TARGET (R2)	GV	MODE	UCB PTR(R1)	UCB PTR(R2)	UCB
DN1						
DN2						
DN3						
...						
DNn						

245

247 248 252 253 254 255

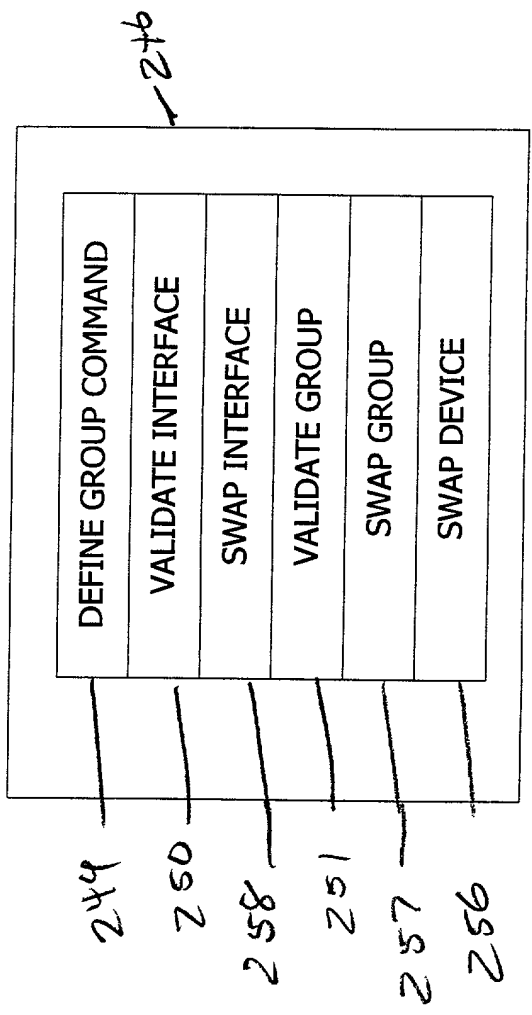


FIG. 3
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TOP SECRET 6099360

244 →

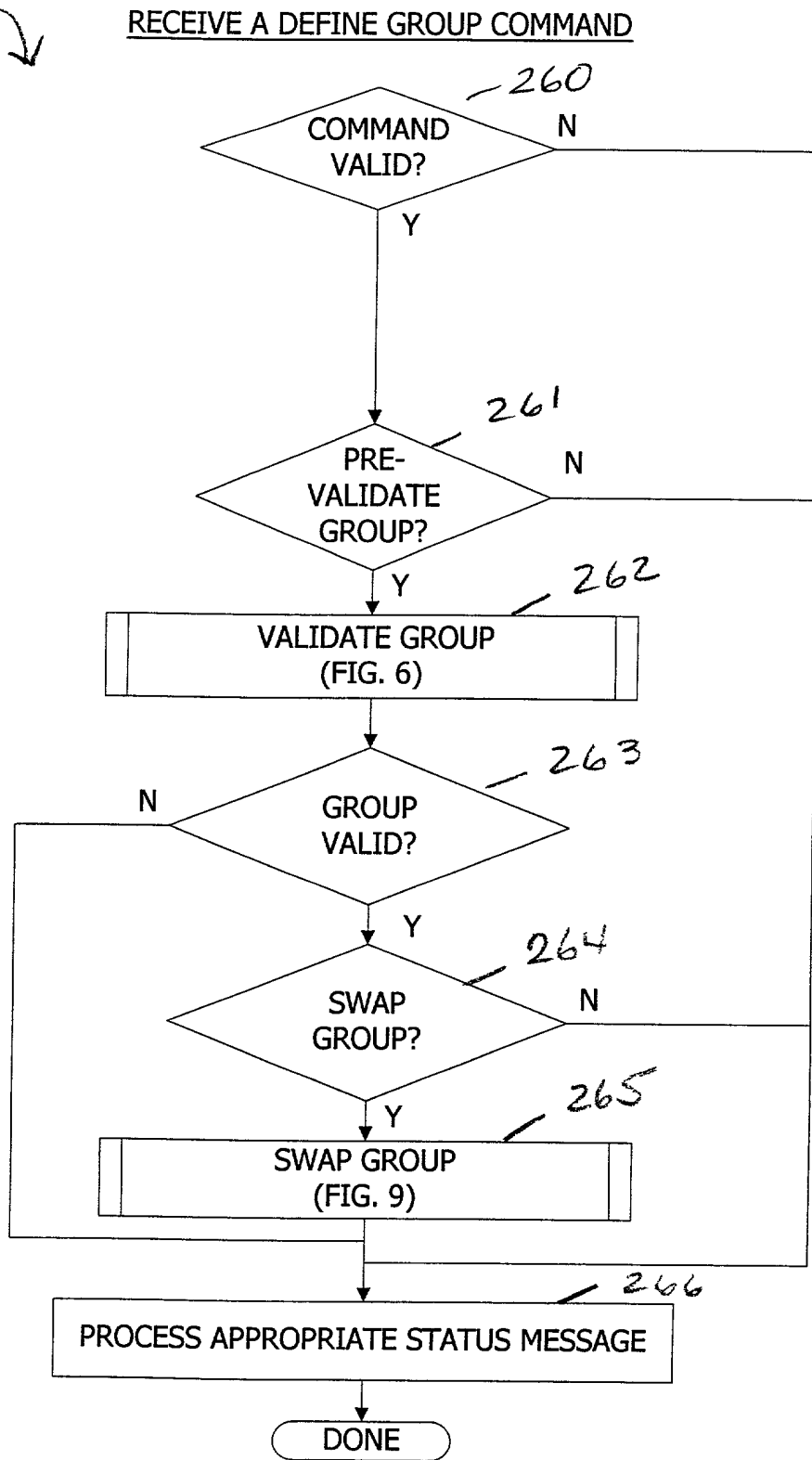


FIG. 4
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VALIDATE INTERFACE

245 ↘

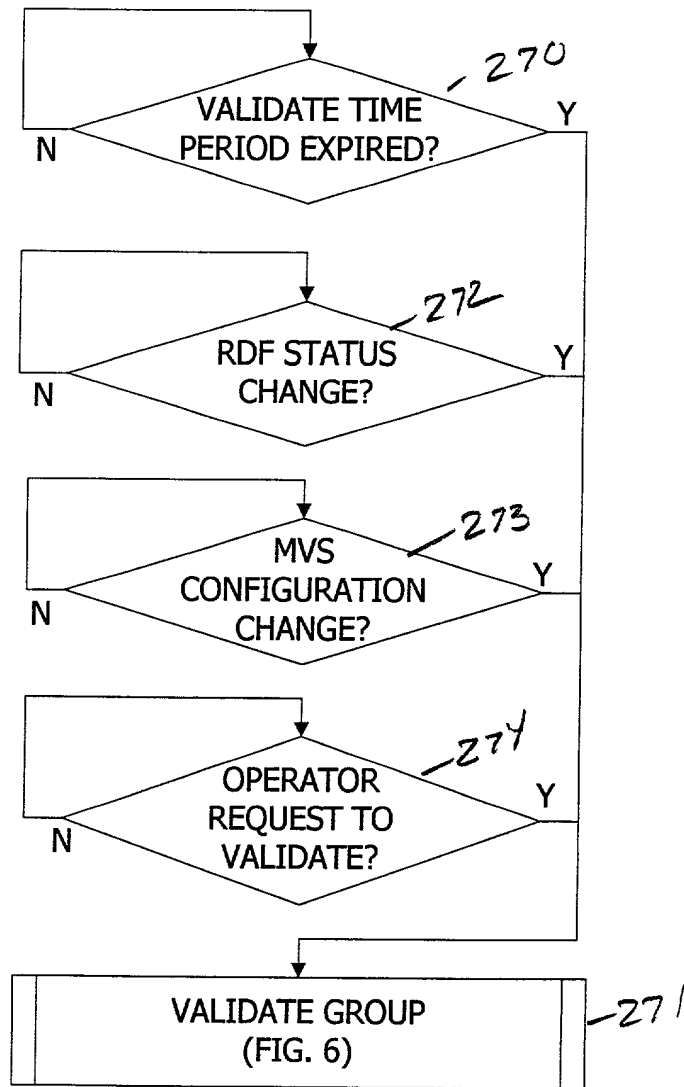


FIG. 5
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FIG. 5 E30-052

VALIDATE GROUP SUBMODULE

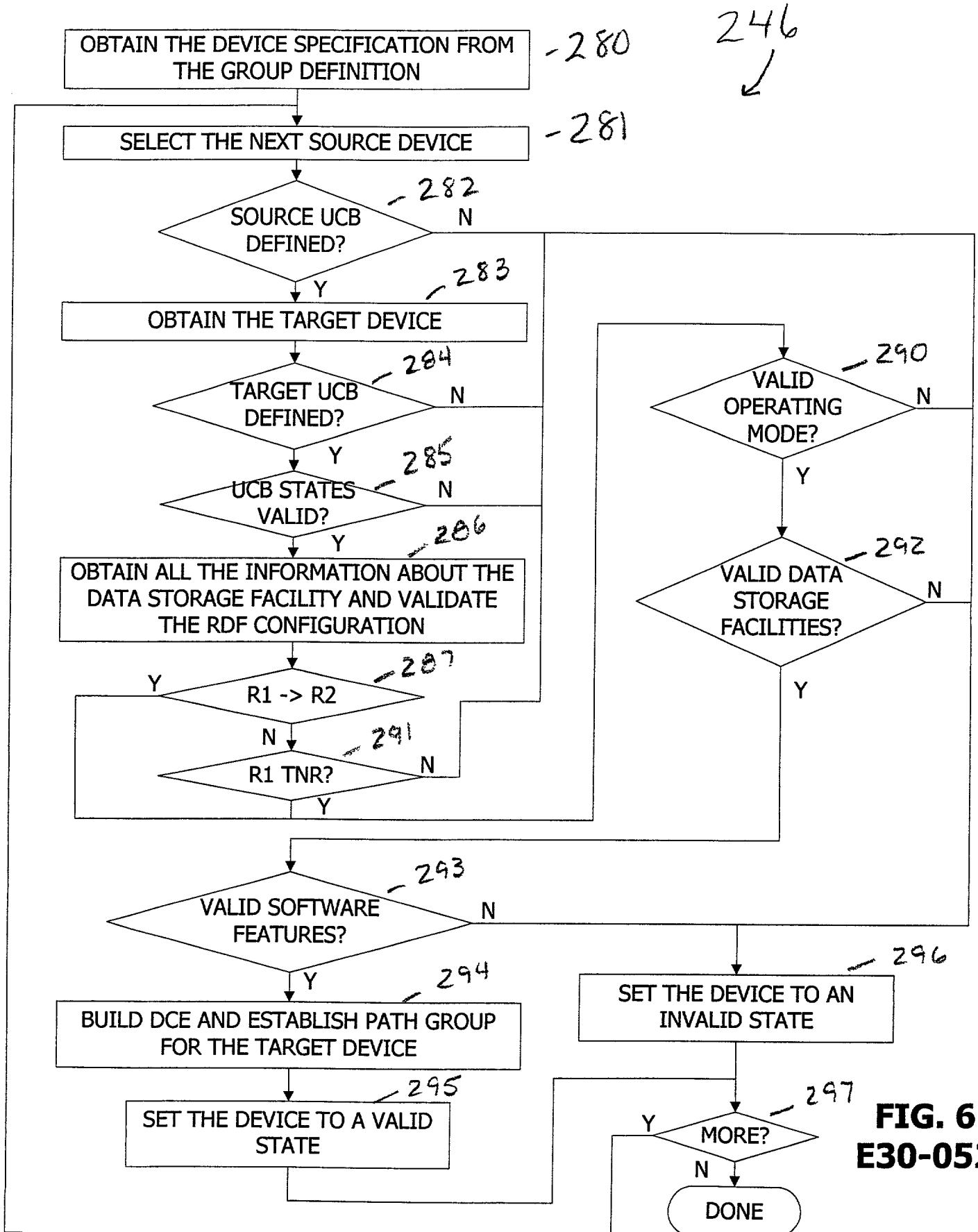


FIG. 6
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SWAP INTERFACE

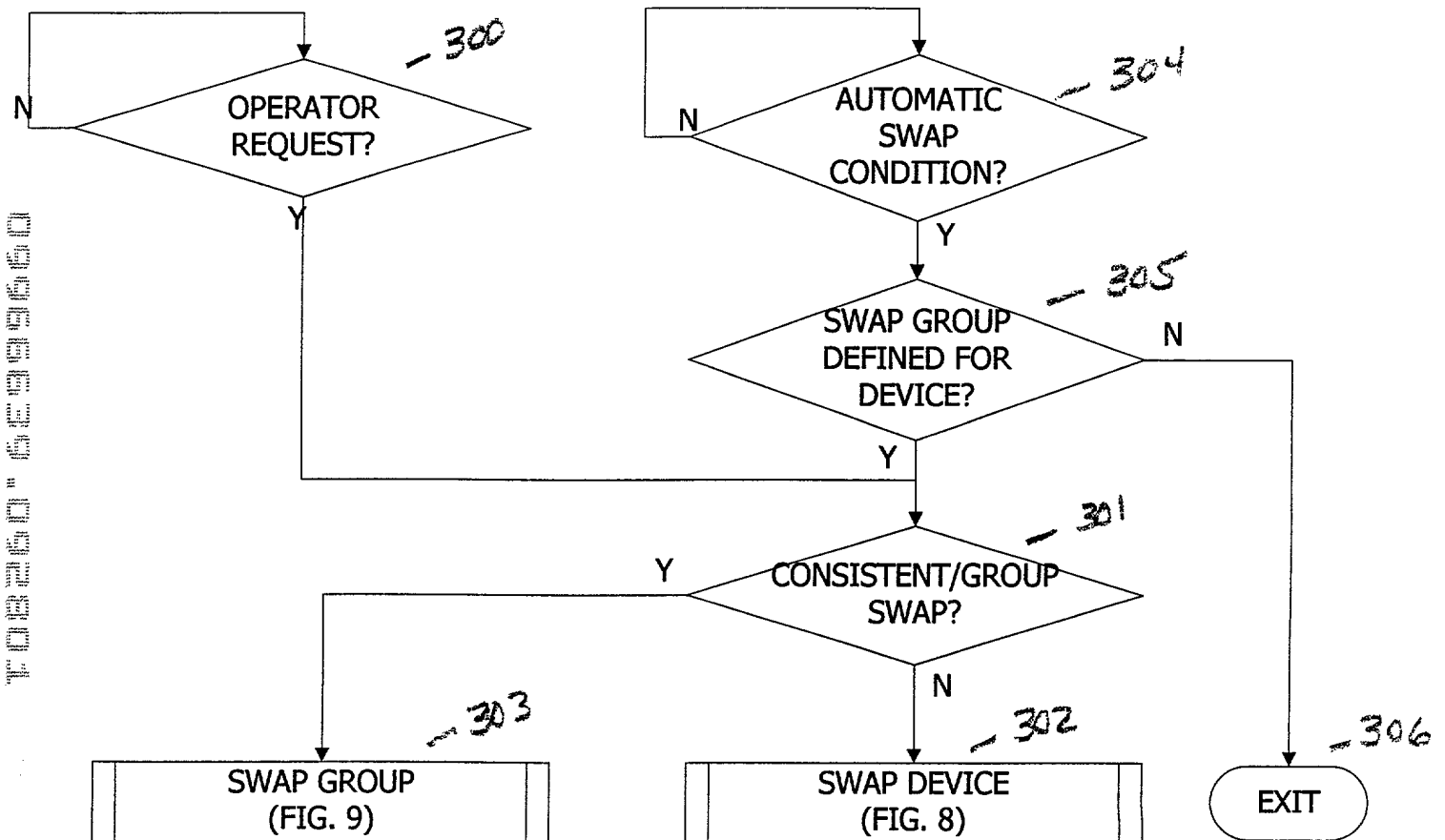


FIG. 7
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SWAP DEVICE

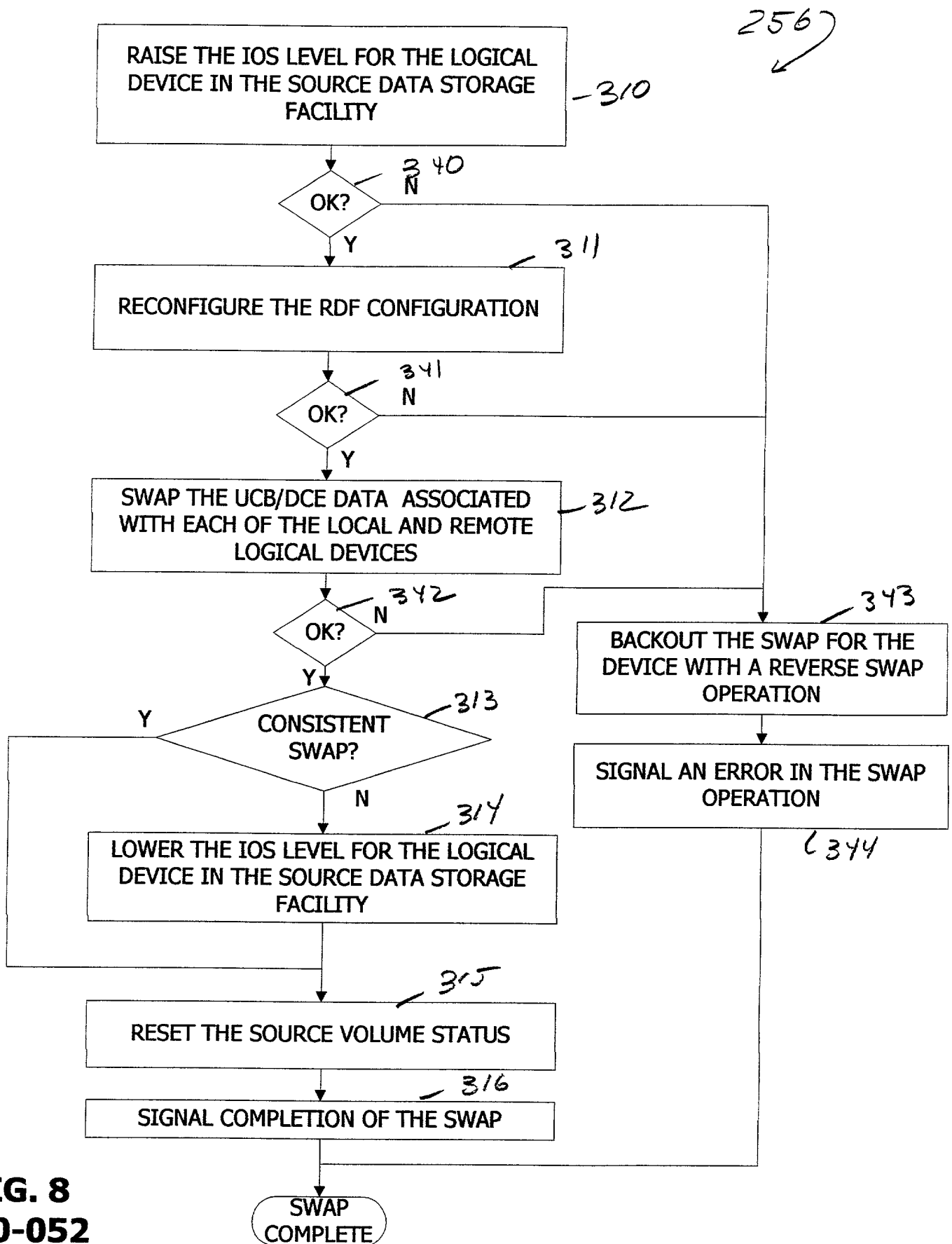


FIG. 8
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SWAP GROUP

